

AN2534

PAC193X Integration Notes for Microsoft[®] Windows[®] 10 and Windows 11 Driver Support

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INTRODUCTION

This document describes the basic steps for integrating the PAC193X DC Power Monitor device in a Microsoft[®] Windows[®] 10 or Windows 11 host system to enable support of the PAC193X Windows Device Driver.

As the PAC193X device can be used in multiple ways and in different system configurations, there are some specific hardware and BIOS configuration details that need to be addressed before loading the Windows device driver.

The details about the PAC193X Windows device driver loading, feature set and software interfaces are included in the "PAC193X Microsoft Windows[®] 10 and Windows 11 Device Driver User's Guide" that complements the information presented by this document.

TABLE 1:GLOSSARY OF TERMS AND
ACRONYMS

Acronym	Term		
E3	Energy Estimation Engine		
EMI	Energy Metering Interface		
OS	Operating System		
CPU	Central Processing Unit		
SoC	System on Chip		
EC	Embedded Controller		
SPB	Simple Peripheral Bus		
SpbCx	SPB Framework Extension		
ACPI	Advanced Configuration and Power Interface		
ASL	ACPI Source Language		
BIOS	Basic Input/Output System		
OEM	Original Equipment Manufacturer		
UUID	Universal Unique Identifier		
DSM	Device-Specific Method		
l ² C	Inter-Integrated Circuit		
SMBus	System Management Bus		

This document is provided as part of the Microchip PAC193X Windows 10 or Windows 11 device driver release and is subject to change with new releases of the driver.

The document revision is Revision C.

HARDWARE INTEGRATION

The hardware integration must first address all the electrical details specified in the device data sheet. For example, the device V_{DD} I/O must match the I²C bus voltage. The following hardware notes address only the hardware details that need specific configuration to make them compatible with the Windows 10 or Windows 11 device driver:

- I²C bus controller Windows support
- PAC193X V_{DD} and SLOW/ALERT pin connections
- · Channel shunt resistor values
- Channel polarity

I²C Bus Controller Windows Support

The PAC193X device I^2C /SMBus interface is, by default, configured in the I^2C mode. Keep in mind that the SMBus protocol is not currently supported by Windows; therefore, the system integrators must connect the PAC devices to the I^2C bus controllers for which a Windows device driver is available and the driver is compatible with the Windows Simple Peripheral Bus Framework Extension (SpbCx). This is a mandatory condition because the PAC193X Windows Device Driver uses the SpbCx standard software interface to communicate with I^2C clients.

The I²C bus controller may be located in the system CPU (SoC architecture), in the system Embedded Controller (EC), or may be another device connected to one of the system internal buses.

PAC193X V_{DD} and SLOW/ALERT Pin Connections

The usual PAC193X system integration scenario assumes that the system power rails are also monitored during the standby or shutdown system states. Therefore, the PAC device V_{DD} pin should be connected to a system power rail that is not shut down by the system standby or power-down sequence. This way, the PAC device continues to be capable of accumulating the data about the system energy consumption during the standby/shutdown state, if desired. The PAC193X Windows device driver is able to retrieve and report these data to the Windows Energy Estimation Engine (E3) or user applications.

In order to facilitate a longer period in standby/ shutdown state, reducing the chances for accumulator register overflow, the device sample rate should be reduced to a minimum. This also reduces energy use by the PAC193X device. In normal system operation, this is done by the Windows device driver that reconfigures the Sample Rate bits from the PAC193X device CTRL register just before the operating system switches the system in the standby/shutdown state. In case of an operating system crash, eventually the system is forcibly shut down by the user or by some automated safety mechanism. The PAC device driver is not functional in case of a Windows system crash, but the PAC device can still be reconfigured in Slow Sampling mode if the SLOW pin is driven high. The SLOW pin can be driven high by a hardware block that detects the system power-off. For example, the SLOW pin can be driven by a transistor that is gated by the CPU or by the main memory power rails. Alternatively, the SLOW pin may be driven by the system Embedded Controller. The EC is usually an always on device, which controls various hardware low-level housekeeping and also controls the system power rails and the power-on sequence.

When the system is turned back on, the PAC193X Windows Device Driver is able to detect that the SLOW pin was asserted and collects the data accumulated during this time period. This is possible because the driver makes sure that the SLOW/ALERT pin is always configured as SLOW and the SLOW pin transitions trigger the Accumulator registers' limited refresh (these are PAC device default settings).

Channel Sense Resistor (R_{SENSE})

Up to the Version 1.3, the PAC193X Windows device driver supports only sense resistor values that are integer values expressed in milliohm units. Therefore, a value of 1.5 milliohms is not supported. Starting with Version 1.3, the driver supports sense resistor values expressed in microohm units. As such, a sense resistor of 1500 microohms is supported.

The recommended shunt resistor tolerance is 1%.

Channel Polarity

After the Power-on Reset (POR), the PAC device default channel configuration is set for unipolar voltage measurements. This configuration can be changed by the Windows device driver initialization phase, as indicated by the DSM Rev. 1 parameters. In addition, the user applications may change the channel polarity settings by calling a dedicated driver interface. However, because the Windows E3 service supports only positive energy values, it is recommended to connect the PAC device SENSE pins to the power rails, such that by default, the V_{SENSE} voltage is read as a positive value. There are two reasons for this:

- Windows E3 service is not able, by design, to control the channel polarity
- The standard Energy Metering Interface (EMI) through which the driver provides the energy figures to E3 service allows only positive integer values

ACPI BIOS INTEGRATION

Over and above any integration guidance provided in this document, OEMs and system integrators are responsible for creating a valid ACPI BIOS that accurately reflects their hardware platform. This includes the details of how the PAC193X is configured and connected in the system.

The guidance provided in this section is limited to describing the details that are necessary to integrate the PAC193X device that is part of a system with the PAC193X device driver and the Windows 10 or Windows 11 operating systems.

Appendix A: "Device Definition Code" of this document contains an example ACPI device definition, written in ACPI Source Language (ASL). The example illustrates how a PAC193X is to be integrated into the ACPI BIOS. Please refer to the Appendix A example when reading this section.

Appendix B: "Test the PAC193X ACPI Integration" of this document provides an example of easily available tools and the process to follow in order to quickly test the PAC193X integration into the system, without replacing the BIOS firmware in the system Flash.

PAC193X Device Description

There are several per-device and per-channel configuration options that are defined when a PAC193X is built into a system. These options include:

- The I²C address for each PAC193X device in the system;
- The name of the power rail to which each PAC193X channel is connected;
- The R_{SENSE} value configured for each channel;
- For which of the PAC193X channels the Windows driver must not create the Energy Metering Interface (EMI) (channels defined as private channels).

These options, which are chosen on a per-platform basis, need to be communicated to the PAC193X device driver. This allows the driver to properly inform Windows about system power usage via the standard Windows EMI.

The method for communicating these configuration options to the PAC193X driver is via the PAC193X device definition in the platform's ACPI BIOS. The OEM is responsible for providing a proper ACPI device definition for each PAC193X device configured in the system. Creating a proper definition for the PAC193X device includes providing the device-specific configuration items outlined in this document.

The definition for the PAC193X device may be copied directly from the **Appendix A: "Device Definition Code**" example, multiplied and customized for use on a particular system.

As part of the PAC193X device definition, a DSM (Device-Specific Method) control method that corresponds to the Universal Unique Identifier (UUID), 033771E0-1705-47B4-9535-D1BBE14D9A09, must be specified as shown in the example. This UUID is reserved to Microchip for the PAC193X and must not be changed. The PAC193X device driver relies on the DSM control method supporting this UUID.

There are six functions implemented by the current DSM Rev. 1. Function #1 is implemented by DSM Rev. 1 the same way as it was implemented by DSM Rev. 0 in order to comply with the ACPI DSM backward compatibility specification.

The PAC193X driver support for DSM Rev. 1 has been implemented starting with Driver Version 1.3.

DSM Function #1 (again, refer to the example in Appendix A: "Device Definition Code") returns a package that describes how each channel on the PAC193X device described by this ACPI device definition is configured. For each channel, the name of the power rail to which the PAC193X channel is connected must be supplied and the R_{SENSE} value configured for that channel.

Note that the supplied power rail names cannot be arbitrary. They must be formed using predefined values that are valid metered hardware names according to the Windows-defined EMI power rail taxonomy. Please refer to Microsoft supplied documentation for a more thorough description of valid power rail names.

The R_{SENSE} value is specified in milliohms and only integer values are allowed.

To indicate that a particular channel is not used on a given PAC193X device (that is, no power calculations should be performed for it), set the channel's R_{SENSE} value to zero.

It is possible to use one or more PAC193X channels to monitor power rails without exposing those measurements to the Windows Energy Metering Interface. Such channels are referred to as 'Private' channels. The PAC193X device driver will calculate the power usage for Private channels too, but will not create the standard EMI interface, and so the results will not be accessible to Windows. Still, the data calculated for a Private channel can be queried by user applications using the Microchip-defined PAC193X device-specific IOCTLs. Before the PAC193X Driver Version 1.3, the only way to configure a channel as 'Private' (it has no EMI) was to set the DSM Function #1 to return an empty string (that is, "") for the channel name. Starting with Driver Version 1.3, all the channels may have names because the DSM Function #3 can be used to indicate which channels are EMI or Private. Note that the R_{SENSE} value for Private channels must be valid and must not be zero.

OEMs and systems integrators must take great care in properly configuring the values for power rails and R_{SENSE} , because both Windows and the PAC193X device driver rely on these values being correct. There is no way for the PAC193X driver to know if a given power rail name is valid, or more importantly, to know if the indicated PAC193X channel is actually connected to the specified power rail.

DSM Function #2 returns a package containing the sense resistor values expressed in microohm units. To improve the data precision, a driver version that supports this function may use the returned microohm values instead of the milliohm values returned by Function #1.

DSM Function #3 returns a bit mask indicating which channels may have EMI interfaces or may be Private, even if they have valid names. Note that the PAC193X driver may not create EMI interfaces for the channels with no name.

DSM Function #4 returns the channel polarity bit mask (V_{BUS} and V_{SENSE} polarity) in the same format as the device NEG_PWR register.

DSM Function #5 returns the sample frequency values that the driver may use to configure the device when the system enters the ACTIVE mode, respectively when the system exits the ACTIVE mode.

DSM Function #6 returns the value of the maximum time interval that the driver may allow between consecutive device REFRESH commands, in order to avoid accumulator saturation.

Parameter	Туре	Value	Comments
_UID	Integer	≥ 1	Each PAC device in the system must have a unique _UID.
I ² C 7-Bit Client Address	Integer	The value selected by the ADDRSEL pin	The Address Select Resistor table from the device data sheet provides the supported address values and their associated resistor values. There are 16 possibilities.
I ² C Connection Speed	Integer	Typ: 400000 Max: 1000000	The selected speed must match the I ² C bus controller supported speed.
I ² C Controller Name	string	The ACPI name of the I ² C bus controller	The I ² C bus controller driver must have the Windows [®] SpbCx compliant driver.
Channel Name	string	"" or non-empty string	 "": Empty string denotes a Private channel. Non-empty string must comply with Microsoft[®] power rail naming taxonomy.
R _{SENSE} Value	Integer	0 or non-zero	 Expressed in milliohms when returned by Function #1. Expressed in microohms when returned by Function #2 (DSM Version 1 feature). 0: The channel is not connected. No data are reported for this channel. Non-zero: The value of the shunt resistor, must fit into a 32-bit unsigned integer.
EMI Enable/Disable Mask	Integer	0x0 ≤ bit mask ≤ 0xF	 Per channel bit allocation: CH1:CH2:CH3:CH4 Bit values: CHn = 1: EMI enabled for Channel 'n' CHn = 0: EMI disabled for Channel 'n' (Private channel) If CHn = 1 but the channel name string is NULL (""), the EMI interface cannot be created by the driver, so the channel remains "Private".
Channel Polarity	Integer	0x00 ≤ bit mask ≤ 0xFF	 This parameter is the initialization value for the device NEG_PWR register (1Dh). Per channel bit allocation: CH1_BIDI : CH2_BIDI : CH3_BIDI : CH4_BIDI : CH4_BIDV : CH2_BIDV : CH3_BIDV : CH4_BIDV Bit values: CHn_BIDI = 0: V_{SENSE} for Channel 'n' is unipolar CHn_BIDI = 1: V_{SENSE} for Channel 'n' is bipolar CHn_BIDV = 0: V_{BUS} for Channel 'n' is unipolar CHn_BIDV = 1: V_{BUS} for Channel 'n' is bipolar
"ACTIVE" Sample Frequency	Integer	Valid Values = {1024, 256, 64, 8}	Sample frequency to be used when the system is "ACTIVE". If the parameter value is not correct, the driver must keep the device POR default – 1024 Hz.

Parameter	Туре	Value	Comments
"IDLE" Sample Frequency	Integer	Valid Values = {1024, 256, 64, 8}	Sample frequency to be used when the system is in a Low-Power mode (SLEEP, CONNECTED-STANDBY, HIBERNATE, OFF). If the parameter value is not correct, the driver must use 8 Hz (same sps as when device SLOW pin is activated) in order to minimize the risk of accumulator saturation during long periods of system low power. (Note : if possible, the activation of the SLOW pin by hardware design ensures the 8 Hz sample rate during Low-Power mode.)
REFRESH Watchdog Timer Interval	Integer	60 ≤ time ≤ 900	 The value is expressed in seconds. The driver must ensure that the time elapsed in "ACTIVE" mode between two consecutive REFRESH commands is not larger than this parameter. If there is no pending driver service request which needs a device REFRESH command to be serviced, the driver must initiate, by itself, a REFRESH in order to reset the device accumulators. The driver must ignore an invalid parameter value and use the hard-coded default (240 seconds in Driver Versions up to 1.3).

TABLE 2: PAC193X ACPI INTEGRATION PARAMETERS SUMMARY (CONTINUED)

REVISION HISTORY

Revision C (January 2022)

Added the PAC193X Windows Device Driver support for Windows 11. The information in this document applies to the PAC193X driver releases 1.4.1, 1.4.2 and later.

Revision B (June 2019)

The information in this document applies to the PAC193X driver releases up to Release 1.4.1. The new release introduces the ACPI DSM Rev. 1.0 for MCHP1930 devices.

Revision A (June 2017)

The information in this document applies to the PAC193X Driver Releases: 1.0, 1.1 and 1.2.

APPENDIX A: DEVICE DEFINITION CODE

EXAMPLE A-1: SAMPLE ACPI PAC193X DEVICE DEFINITION

```
Device (PA01)
   Name( HID, "MCHP1930")
   Name(UID, 1)
    // Lowest power D-State supported by the device is D3
   Name ( SOW, 3)
    // Device Status: present, enabled, and functioning properly
   Method( STA, 0x0, NotSerialized)
    {
       Return(0xf)
   }
    // Current Resources Settings
   Method( CRS, 0x0, NotSerialized)
    {
        Name(RBUF, ResourceTemplate()
        {
                                                    // 7-bit Client Address
// Client or host?
            I2CSerialBus(0x10,
                    ControllerInitiated,
                                                    // Connection Speed in hz
// 7-bit or 10-bit addressing?
                    400000,
                    AddressingMode7Bit,
                                                    // I2C Controller to which PAC is connected
// Resource Index
                    "\\_SB.PCI0.I2C0",
                    Ο,
                                                    // Consumer or Producer?
                    ResourceConsumer)
        })
       Return (RBUF)
   }
    // DSM - Device Specific Method
    11
    // This method returns configuration information that tells the driver
    // which devices each line in the PAC193x is wired to monitor.
   // Names of the monitored devices must come from the Microsoft-defined
   // power rail taxonomy.
   11
   // The UUID for the Microchip PAC193x's DSM is {033771E0-1705-47B4-9535-D1BBE14D9A09}.
   // This is unique to the device, and must match what the Windows PAC193x driver expects.
    11
    // Returns:
               Either: A Buffer (for supported Functions, or an error)
    11
   11
                        A Package containing PAC1934 resources (rail/resistor values/configs)
    11
      Input Arguments (per _DSM standard):
Arg0: UUID - Function Identifier
    11
    11
           Arg1: Integer - Revision
Arg2: Integer - Function Index
    11
    11
           Arg3: Package - Parameters (not used in our implementation)
    11
    11
   Function(DSM, {BuffObj, PkgObj}, {BuffObj, IntObj, IntObj, PkgObj})
           Is our UUID being invoked?
        11
       if(LNotEqual(Arg0, ToUUID("033771E0-1705-47B4-9535-D1BBE14D9A09")))
        {
            return(Buffer() {0x0})
                                                    // incorrect UUID, return NULL for error
        }
        // Function number check
       switch(ToInteger(Arg2))
        {
            // Function 0 returns a bit-mask of supported functions
            case(0)
                switch(ToInteger(Arg1))
                                                    // revision check
                {
                    // Revision 0: function 1 is supported
                    case(0) {return (Buffer() {0x3})}
                    // Revision 1: functions 1->6 are supported
                    case(1) {return (Buffer() {0x7f})}
                ι
                break;
            }
```

EXAMPLE A-1: SAMPLE ACPI PAC193X DEVICE DEFINITION (CONTINUED)

```
// Function 1 returns the channel "Rail Name" and
// the Resistor Value expressed in milli-Ohms.
case(1)
   // All Revisions supported (0 and 1 presently defined)
   Name(BUF1, Package()
   {
       // Rail Name,
                       Resistor Value
                ,
                                        // Channel 1
       "GPU"
                         24900,
       "CPU CORES" ,
                                        // Channel 2
                          49900,
       "SENSORS" ,
                                        // Channel 3
// Channel 4
                           75000,
                         100000
       "WIFI"
                   .
   })
   return(BUF1)
}
// Function 2 returns the Resistor values expressed in micro-Ohms.
case(2)
{
   If(LLess(Arg1,1)) { break; } // Revision 0 not supported.
   // Return enhanced precision resistor values.
   Name(BUF2, Package()
    {
       // Value
        24900000,
                             // Channel 1
        49900000,
                             // Channel 2
                             // Channel 3
        75000000,
       100000000
                              // Channel 4
   })
   return(BUF2)
}
// Function 3 returns the EMI enabled/disabled flags
case(3)
{
   If(LLess(Arg1,1)) { break; } // Revision 0 not supported.
   // Return ON/OFF flags.
   Name(BUF3, Package()
    {
                   // driver EMI ENABLE mask, CH1:CH2:CH3:CH4 - 1b=ON, Ob=OFF
       0xF
                   // EMI mask bit set but rail name is NULL -> EMI is not created (private channel)
                   // channel name valid but mask bit not set -> EMI is not created (private channel)
   })
   return(BUF3)
}
// Function 4 returns the channel bipolar settings
case(4)
{
                                        // Revision 0 not supported.
   If(LLess(Arg1,1)) { break; }
   // Return the target NEG PWR flags.
   Name(BUF4, Package()
    {
                                          // CH1:CH2:CH3:CH4-BIDI : CH1:CH2:CH3:CH4-BIDV
       0x00
   })
   return(BUF4)
}
```

```
EXAMPLE A-1: SAMPLE ACPI PAC193X DEVICE DEFINITION (CONTINUED)
```

```
// Function 5 returns the SPS for ACTIVE and IDLE
           case(5)
           {
              If(LLess(Arg1,1)) { break; } // Revision 0 not supported.
              Name(BUF5, Package()
              {
                  1024,
                                             // ACTIVE sps - accepted values = {1024, 256, 64, 8}
                                             // IDLE sps - accepted values = {1024, 256, 64, 8}
                  8
              })
              return (BUF5)
           }
           // Function 6 returns the watchdog interval
          case(6)
           {
              If(LLess(Arg1,1)) { break; } // Revision 0 not supported.
              Name(BUF6, Package()
               {
                  900
                                            // seconds (min=60, max=900)
              })
              return(BUF6)
           }
       } // switch(Arg2)
       // Return an error (a buffer with a value of zero)
       // if we didn't return anything else above
       return(Buffer() {0x0})
   } // _DSM
} // Device(PA01)
```

APPENDIX B: TEST THE PAC193X ACPI INTEGRATION

CAUTION: The tools and the process presented in this appendix are not the only method to integrate the PAC193X ASL, compile and deploy it on a target system. This method does not replace the original system BIOS firmware from the Flash and works only if Windows 10 or Windows 11 OS on the target system is configured in Test mode. The method works only if the target system already has Windows 10 or Windows 11 installed on it and the user has the possibility to install and run on it the presented tools. ASL code changes and deployment must be done with great care because of the potential risk to cause the Windows OS to crash and the loss of data.

The Tools

- iASL and AcpiDump tools from ACPICA: https://acpica.org/downloads/binary-tools
- ASL tool from Microsoft Windows Driver Kit (WDK): https://docs.microsoft.com/en-us/windows-hardware/ drivers/bringup/microsoft-asl-compiler (use the 64-bit or the 32-bit version, depending on the target system OS type)
- BCDEdit utility, installed by default in Windows 10 or Windows 11
- Notepad utility (installed by default in Windows 10 or Windows 11) or any other text editor that runs on the target system
- Shutdown utility (installed by default in Windows 10 or Windows 11)

The Process

- 1. Copy the tools and the sample ASL code to the target system:
 - Create a working folder (ex: ACPI_MCHP1930) and copy the PAC193X ASL sample code in a simple text file.
 - Copy the ASL and the ACPICA tools in the working folder or, alternatively, create for them a dedicated Tools folder and put it in the system path.
- 2. Dump the target system ACPI code:
 - Open an elevated command prompt (administrator command prompt) and change the working directory to the working folder just created.
 - Use the AcpiDump tool to obtain the system ACPI tables in binary format:

C:\ACPI_MCHP1930>acpidump -b

Several files will be created on the working folder, one for each ACPI table type, having the name of the contained ACPI table and the .dat extension.

- 3. Disassemble the binary ACPI code:
 - Use the iASL tool to disassemble the binary ACPI code into ASL code:

C:\ACPI MCHP1930>iasl -d *.dat

Several files will be created on the working folder, one for each ACPI table type, having the name of the contained ACPI table and the .dsl extension.

- 4. Add the PAC193X ASL code into ACPI SSDT:
 - Open the ssdt.dsl file in the text editor, add the PAC193X ASL sample code and modify it to match the target system architecture.

Tip: Look for the I²C controller ACPI definition in DSDT or SSDT and note the ACPI name. This name is needed to define the PAC193X device I²C connection; see the I²C Serial Bus section from **Appendix A: "Device Definition Code**".

- 5. Compile the modified SSDT:
 - Use iASL tool to generate the binary SSDT code:

C:\ACPI_MCHP1930>iasl ssdt.dsl

The file, ssdt.aml, is generated if there is no compilation error.

6. Load the new ACPI SSDT in the Windows registry.

Windows acpi.sys driver provides the ACPI code developers the option to override the original ACPI code from the system Flash with a different version saved in the Windows registry.

• Use the ASL tool to save the ACPI table into the Windows registry:

```
C:\ACPI_MCHP1930>asl /loadtable -v ssdt.aml
```

Note: This command works only from the elevated command prompt.

Tip: In case the Windows registry already contains an ACPI code override, it must be "unloaded" before loading the new version. Provide the option, "-d", to the ASL tool in order to achieve this:

```
C:\ACPI_MCHP1930>asl /loadtable -v -d ssdt.aml
```

7. Switch Windows in Test mode.

The ACPI table override feature presented above is enabled only in Windows **Test mode**.

Use the BCDEdit utility to enable the Test mode:

C:\ACPI_MCHP1930>bcdedit /set testsigning ON

Tip: In order to disable the Test mode, replace ON with OFF in the above example.

- 8. Reboot the system
 - Use the Shutdown utility in order to make sure that a full power cycle is executed, not affected by the Windows 10 or Windows 11 Fast Start facility:

C:\ACPI MCHP1930>shutdown -r -t 0

9. Check the Device Manager.

At this stage, because there is no driver installed yet, the PAC193X devices should be enumerated by the Windows **Device Manager** utility under **Unknown Devices** category.

10. Install the PAC193X Windows device driver.

Read and follow the steps presented by the Driver installation section from "PAC193X Microsoft Windows" 10 and Windows 11 Device Driver User's Guide".

CAUTION: If a BIOS upgrade or a BIOS configuration change is needed, the following is recommended:

- First, disable the ACPI code override, either by unloading the code from the Windows registry (see Step #6) or by turning off the Test mode (see the Step #7).
- Repeat the steps listed above, starting with Step #2.

NOTES:

Note the following details of the code protection feature on Microchip products:

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