## Physics 120/220



# Voltage Divider Capacitor RC circuits 

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## Voltage Divider

The figure is called a voltage divider.
It's one of the most useful and important circuit elements we will encounter.

It is used to generate a particular voltage for a large fixed $V_{i n}$.
$\operatorname{Current}\left(\mathrm{R}_{1} \& \mathrm{R}_{2}\right) \quad I=\frac{V_{\text {in }}}{R_{1}+R_{2}}$

Output voltage:


$$
V_{\text {out }}=I R_{2}=\frac{R_{2}}{R_{1}+R_{2}} V_{\text {in }} \quad \therefore V_{\text {out }} \leq V_{\text {in }}
$$

Voltage drop is proportional to the resistances
$\mathrm{V}_{\text {out }}$ can be used to drive a circuit that needs a voltage lower than $\mathrm{V}_{\text {in }}$.

## Voltage Divider (cont.)

Add load resistor $\mathrm{R}_{\mathrm{L}}$ in parallel to $\mathrm{R}_{2}$.
You can model $R_{2}$ and $R_{L}$ as one resistor (parallel combination), then calculate $V_{\text {out }}$ for this new voltage divider

If $\mathrm{R}_{\mathrm{L}} \gg \mathrm{R}_{2}$, then the output voltage is still: $\quad V_{L}=\frac{R_{2}}{R_{1}+R_{2}} V_{\text {in }}$
However, if $R_{L}$ is comparable to $R_{2}, V_{L}$ is reduced. We say that the circuit is "loaded".


## Ideal voltage and current sources

Voltage source: provides fixed $\mathrm{V}_{\text {out }}$ regardless of current/load resistance.
Has zero internal resistance (perfect battery).
Real voltage source supplies only finite max I.

(a) ideal voltage source

(b) real voltage source including internal resistance $r$

Current source: provides fixed $\mathrm{I}_{\text {out }}$ regardless of voltage/load resistance.
Has infinite resistance.
Real current source have limit on voltage they can provide.

## Voltage source

- More common
- In almost every circuit
- Battery or Power Supply (PS)

(c) ideal current source

(d) real current source including shunt resistance $r$


## Thevenin's theorem

Thevenin's theorem states that any two terminals network of $\mathrm{R} \& \mathrm{~V}$ sources has an equivalent circuit consisting of a single voltage source $\mathrm{V}_{\mathrm{TH}}$ and a single resistor $\mathrm{R}_{\mathrm{TH}}$.


To find the Thevenin's equivalent $\mathrm{V}_{\mathrm{TH}} \& \mathrm{R}_{\mathrm{TH}}$ :

- For an "open circuit" $\left(\mathrm{R}_{\mathrm{L}} \rightarrow \infty\right)$, then

$$
V_{\mathrm{Th}}=V_{\text {open circuit }}
$$

- Voltage drops across device when disconnected from circuit - no external load attached.
- For a "short circuit" $\left(\mathrm{R}_{\mathrm{L}} \rightarrow 0\right)$, then

$$
R_{\mathrm{Th}}=\frac{V_{\text {open circuit }}}{I_{\text {short circuit }}}
$$

- $\mathrm{I}_{\text {short circuit }}=$ current when the output is shorted directly to the ground.


## Thevenin's theorem (cont)



Thevenin equivalent

Open circuit voltage: $V_{T H}=V_{\text {out }}=V_{\text {in }} \frac{R_{2}}{R_{1}+R_{2}} \quad \longleftarrow$ Lower leg of divider Short circuit current: $\quad I_{\text {short tircuit }}=\frac{V_{i n}}{R_{1}}$

## Thevenin equivalent:

Voltage source:

$$
V_{T H}=V_{i n} \frac{R_{2}}{R_{1}+R_{2}}
$$

V open-circuit - no external load
in series with:

$$
R_{T H}=\frac{R_{1} R_{2}}{R_{1}+R_{2}}
$$

"like" $\mathrm{R}_{1}$ in parallel with $\mathrm{R}_{2}$
$R_{\text {Th }}$ is called the output impedance $\left(Z_{\text {out }}\right)$ of the voltage divider

## Thevenin's theorem (cont)

Very useful concept, especially when different circuits are connected with each other. Closely related to the concepts of input and output impedance (or resistance).


Circuit A, consisting of $\mathrm{V}_{\mathrm{TH}}$ and $\mathrm{R}_{\mathrm{TH}}$, is fed to the second circuit element $B$, which consists of a simple load resistance $R_{L}$.

## Avoiding circuit loading

The combined equivalent circuit ( $\mathrm{A}+\mathrm{B}$ ) forms a voltage divider:

$$
V_{\text {out }}=V_{T H} \frac{R_{L}}{R_{T H}+R_{L}}=\frac{V_{T H}}{1+\left(R_{T H} / R_{L}\right)}
$$

$\mathrm{R}_{\mathrm{TH}}$ determines to what extent the output of the $1^{\text {st }}$ circuit behave as an ideal voltage source.

Circuit A


To approximate ideal behavior and avoid loading the circuit, the ratio $R_{T H} / R_{L}$ should be kept small.
10 X rule of thumb: $\mathrm{R}_{\mathrm{TH}} / \mathrm{R}_{\mathrm{L}}=1 / 10$

The output impedance of circuit $A$ is the Thevenin equivalent resistance $R_{T H}$ (also called source impedance).
The input impedance of circuit B is its resistance to ground from the circuit input. In this case, it is simply $R_{L}$.

## Example: voltage divider

$\mathrm{V}_{\mathrm{in}}=30 \mathrm{~V}, \mathrm{R}_{1}=\mathrm{R}_{2}=\mathrm{R}_{\text {load }}=10 \mathrm{k}$
a) Output voltage w/ no load [Answ 15v]

b) Output voltage w/ 10k load [Answ 10v]


## Example (cont.)

c) Thevenin equivalent circuit $\left[\mathrm{V}_{\mathrm{TH}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{TH}}=5 \mathrm{k}\right]$

d) Same as b) but using the Thevenin equivalent circuit [Answ 10v]
e) Power dissipated in each of the resistor $\left.{ }_{[A n s w} \mathrm{P}_{\mathrm{R} 1}=0.04 \mathrm{~W}, \mathrm{P}_{\mathrm{R} 2}=\mathrm{P}_{\mathrm{RL}}=0.01 \mathrm{~W}\right]$

## Example: impedance of a Voltmeter

We want to measure the internal impedance of a voltmeter.
Suppose that we are measuring $\mathrm{V}_{\text {out }}$ of the voltage divider:


- $\mathrm{R}_{\text {TH }}: 2100 \mathrm{k}$ in parallel, $100 \mathrm{k} / 2=50 \mathrm{k}$
- $V_{T H}=20 \frac{100 \mathrm{k}}{2 \times 100 \mathrm{k}}=10 \mathrm{~V}$
- Measure voltage across $\mathrm{R}_{\text {in }}\left(\mathrm{V}_{\text {out }}\right)=8 \mathrm{~V}$, thus 2 V drop across $\mathrm{R}_{\mathrm{TH}}$
- The relative size of the two resistances are in proportion of these two voltage drops, so $R_{\text {in }}$ must be $4(8 / 2) R_{T H}$, so $R_{\text {in }}=200 \mathrm{k}$


## Terminology

## Terminology (cont)

## Offset = bias

A DC voltage shifts an AC voltage up or down.


AC signal


AC signal with DC offset

## Gain:

$$
A_{V}=\frac{V_{\text {out }}}{V_{\text {in }}}
$$

$$
A_{I}=\frac{I_{\text {out }}}{I_{\text {in }}}
$$

Voltage gain
Unity gain: $\mathrm{V}_{\text {out }}=\mathrm{V}_{\text {in }}$
Current gain

## Terminology

When dealing with AC circuits we'll talk about V \& I vs time or A vs f .

Lower case symbols:

- i: AC portion of current waveform
- v : $A C$ portion of voltage waveform. $\mathrm{V}(\mathrm{t})=\mathrm{V}_{\mathrm{DC}}+\mathrm{v}$


## Decibels:

To compare ratio of two signals: $d B=20 \log _{10} \frac{\text { amplitude } 2}{\text { amplitude } 1}$

Often used for gain: eg ratio is $1.4 \approx \sqrt{ } 2$
$20 \log _{10} \sqrt{1.4}=3 \mathrm{~dB}$
NB: 3dB ~ power ratio of $1 / 2$
~ amplitude ratio of 0.7

# Capacitors and RC circuits 

## Capacitor: reminder

$Q=C V$
Q : total charge [Coulomb]
C: capacitance [Farad $1 \mathrm{~F}=1 \mathrm{C} / 1 \mathrm{~V}] \quad c=\frac{Q}{V}=\varepsilon_{0} \frac{A}{d}$ [parallel-plate capacitor]
V : voltage across cap
Since $I=\frac{d Q}{d t} \quad I=C \frac{d V}{d t} \quad \begin{aligned} & \text { I: rate at which charge flows } \\ & \text { or rate of change of the voltage }\end{aligned}$


For a capacitor, no DC current flows through, but AC current does.
Large capacitances take longer to charge/discharge than smaller ones.

Typically, capacitances are

- $\mu \mathrm{F}\left(10^{-6}\right)$
- $\mathrm{pF}\left(10^{-12}\right)$

$$
\begin{array}{lll}
C_{e q}=C_{1}+C_{2}+C_{3} & {[\text { parallel] }} & \frac{1}{C_{e q}}=\frac{1}{C_{1}}+\frac{1}{C_{2}}+\frac{1}{C_{3}} \\
\text { Same voltage drop across caps } & \text { All caps have same } \mathrm{Q}
\end{array}
$$

## Frequency analysis of reactive circuit

$$
I(t)=C \frac{d V(t)}{d t}=C \omega V_{0} \cos (\omega t)
$$

ie the current is out of phase by $90^{\circ}$ to wrt voltage (leading phase)


Considering the amplitude only: $I=\frac{V_{0}}{1 / \omega C} \quad \omega=2 \pi f$
Frequency dependent resistance: $R=1 / \omega C=\frac{1}{2 \pi f C}$

Example: $\mathrm{C}=1 \mu \mathrm{~F} \quad 110 \mathrm{~V}(\mathrm{rms}) \quad 60 \mathrm{~Hz}$ power line

$$
I_{r m s}=\frac{110}{1 /\left(2 \pi \times 60 \times 10^{-6}\right)}=41.5 \mathrm{~mA}(\mathrm{rms}) \quad I_{r m s}=\frac{I}{\sqrt{2}}
$$

## Impedance of a capacitor

Impedance is a generalized resistance.
It allows rewriting law for capacitors so that it resembles Ohm's law.
Symbol is Z and is the ratio of voltage/current.
Recall: $I=C \frac{d V}{d t}$

$$
V(t)=V_{0} \cos (\omega t)=\operatorname{Re}\left[V_{0} e^{j \omega t}\right]
$$

$$
\begin{aligned}
& I=C \frac{d}{d t}\left(V_{0} e^{j \omega t}\right)=j \omega C V_{0} e^{j \omega t} \\
& I=\frac{V_{0} e^{j \omega t}}{-j / \omega C}
\end{aligned}
$$

The actual current is: $I=\operatorname{Re}\left[\frac{V_{0} e^{j \omega t}}{Z_{c}}\right] \quad Z_{c}=-j / \omega C$
$Z_{c}$ is the impedance of a capacitor at frequency $\omega$.
As $\omega$ (or f ) increases (decreases), $\mathrm{Z}_{\mathrm{c}}$ decreases (increases)
The fact that $Z_{c}$ is complex and negative is related to the fact the the voltage across the cap lags the current through it by $90^{\circ}$.

## Ohm's law generalized

Ohm's law for impedances: $\quad V(t)=Z I(t) \quad \tilde{V}=\tilde{Z} \tilde{I} \quad$ using complex notation

$$
\begin{array}{cl}
Z_{e q}=Z_{1}+Z_{2}+Z_{3} & {[\text { series }]} \\
\frac{1}{Z_{e q}}=\frac{1}{Z_{1}}+\frac{1}{Z_{2}}+\frac{1}{Z_{3}} \quad[\text { parallel }]
\end{array}
$$

Resistor: $\quad Z_{R}=R \quad$ in phase with I
Capacitor: $\quad Z_{c}=-j / \omega C=1 / j \omega C \quad$ lags I by $90^{\circ}$
Inductor: $\quad Z_{L}=j \omega L$
leads I by $90^{\circ}$ (use mainly in RF circuits)

Can use Kirchhoff's law as before but with complex representation of V \& I.

Generalized voltage divider:

$$
\tilde{V}_{\text {out }}=\tilde{V}_{\text {in }} \frac{\tilde{Z}_{2}}{\tilde{Z}_{1}+\tilde{Z}_{2}}
$$



## RC circuit

Capacitor is uncharged. At $\mathrm{t}=0$, the RC circuit is connected to the battery (DC voltage)
The voltage across the capacitor increases with time according to:

$$
I=C \frac{d V}{d t}=\frac{V_{i}-V}{R} \quad \rightarrow \quad V=V_{i}+A e^{-t / R C}
$$

A is determined by the initial condition:
(a) $\mathrm{t}=0, \mathrm{~V}=0$ thus $\mathrm{A}=-\mathrm{V}_{\mathrm{i}}$

$$
V=V_{i}\left(1-e^{-t / R C}\right)
$$

when $\mathrm{t}=\mathrm{RC} 1 / \mathrm{e}=0.37$
Rate of charge/discharge is
@ 1RC $63 \%$ of voltage
determined by RC constant
@ $5 \mathrm{RC} 99 \%$ of voltage

## Time constant RC:



For R Ohms and C in Farads, RC is in seconds
For $\mathrm{M} \Omega$ and $\mu \mathrm{F}, \mathrm{RC}$ is seconds
For $\mathrm{k} \Omega$ and $\mu \mathrm{F}, \mathrm{RC}$ is ms

Consider a circuit with a charge capacitor, a resistor, and a switch


Before switch is closed, $V=V_{i}$ and $Q=Q_{i}=C V_{i}$
After switch is closed, capacitor discharges and voltage across capacitor decreases exponentially with time

$$
C \frac{d V}{d t}=I=-\frac{V}{R} \quad \rightarrow \quad V=V_{i} e^{-t / R C}
$$

## Differentiator

Consider the series $R C$ circuit as a voltage divider, with the output corresponding to the voltage across the resistor:

V across C is $\mathrm{V}_{\mathrm{in}}-\mathrm{V}$

$$
I=C \frac{d}{d t}\left(V_{i n}-V\right)=\frac{V}{R}
$$



If we choose $\mathrm{R} \& \mathrm{C}$ small enough so that

$$
\frac{d V}{d t} \ll \frac{d V_{i n}}{d t}
$$

then, $\quad V(t)=R C \frac{d}{d t} V_{i n}(t)$
Thus the output differentiate the input waveform!
Simple rule of thumb: differentiator works well if $V_{\text {out }} \ll V_{\text {in }}$
Differentiators are handy for detecting leading edges \& trailing edges in pulse signals.

## Integrator

Now flip the order of the resistor and capacitor, with the output corresponding to the voltage across the capacitor:

V across R is $\mathrm{V}_{\text {in }}-\mathrm{V}$

$$
I=C \frac{d V}{d t}=\frac{V_{i n}-V}{R}
$$



If RC is large, then $\mathrm{V} \ll \mathrm{V}_{\mathrm{in}}$ and

$$
C \frac{d V}{d t} \cong \frac{V_{i n}}{R} \quad \rightarrow \quad V(t)=\frac{1}{R C} \int^{t} V_{i n}(t) d t+c s t
$$

Thus the output integrate the input!
Simple rule of thumb: integrator works well if $\quad V_{\text {out }} \ll V_{\text {in }}$
Integrators are used extensively in analog computation (eg analog/digital conversion, waveform generation etc...)

Let's interpret the differentiator $R C$ circuit as a frequency-dependent voltage divider ("frequency domain"):
Using complex Ohm's law: $\tilde{I}=\frac{\tilde{V}_{\text {in }}}{\tilde{Z}_{\text {boual }}}=\frac{\tilde{V}_{\text {in }}}{R-(/ / \omega C)}=\frac{\tilde{V}_{i n}[R+(j / \omega C)]}{R^{2}+1 / \omega^{2} C^{2}}$
Voltage across R is: $\tilde{V}_{\text {out }}=\tilde{I} R=R \frac{\tilde{V}_{\text {in }}[R+(j / \omega C)]}{R^{2}+1 / \omega^{2} C^{2}}$


Voltage divider made of R \& C

If we care only about the amplitude: $V_{\text {out }}=\left(\tilde{V}_{\text {out }} \tilde{W}_{\text {out }}^{*}\right)^{1 / 2}=V_{\text {in }} \frac{R}{\left[R^{2}+1 / \omega^{2} C^{2}\right]^{1 / 2}}$

- Thus $\mathrm{V}_{\text {out }}$ increases with increasing f Impedance of a series RC combination: $\tilde{Z}_{\text {botal }}=R-j / \omega C$



## High-pass filter frequency response



Output $\sim$ equal to input at high frequency when $\omega \sim 1 / \mathrm{RC}[\mathrm{rad}]$

Goes to zero at low frequency.

High-pass filter frequency response curve
A high-pass filter circuit attenuates low frequency and "passes" the high frequencies.

The frequency at which the filter "turns the corner" (ie $\mathrm{V}_{\text {out }} / \mathrm{V}_{\text {in }}=1 / \sqrt{ } 2=0.7$ ) is called the 3 dB point:

$$
f_{3 d B}=\frac{1}{2 \pi R C}[H z]
$$

occurs when $\mathrm{Z}_{\mathrm{c}}=\mathrm{R}$

Use this in lab
otherwise factor $2 \pi$ off

NB: 3dB ~ power ratio of $1 / 2$
~ amplitude ratio of 0.7

## Low-pass filter

Now simply switch the order of the resistor and capacitor in the series circuit (same order as the integrator circuit earlier):


A low-pass filter circuit attenuates high frequency and "passes" the low frequencies.


Low-pass filter frequency response curve

