## AQA ${ }^{[ }$

## A-level COMPUTER SCIENCE

## Paper 2

TBC
am/pm
2 hours 30 minutes

## Materials

- There are no additional materials required for this paper.


## Instructions

- Use black ink or black ball-point pen.
- Fill in the boxes at the bottom of this page.
- Answer all questions.
- Do all rough work in this book. Cross through any work that you do not want to be marked.
- You may use a calculator.


## Information

- The marks for questions are shown in brackets.
- The maximum mark for this paper is 100 .


## Advice

- In some questions you may be required to indicate your answer by shading a lozenge. If you wish to change your answer make sure that the incorrect answer is clearly crossed through with an x .

Please write clearly, in block capitals, to allow character computer recognition.
Centre number $\square$ Candidate number $\square$
Surname $\square$ Candider numblor

Forename(s) $\square$

Candidate signature $\qquad$

Answer all questions in the spaces provided.

Figure 1 shows how some of the components of a computer system can be connected together.

Figure 1


Table 1 lists the names of six components in the column headings and the five letters (A-E) from Figure 1 in the row headings.

For each row in Table 1, shade one lozenge, in the appropriate column, to indicate which component in Figure 1 has been labelled with the row letter.

As an example, the first row has been completed for you, to indicate that component $\mathbf{A}$ in Figure 1 is the Address bus.
[4 marks]
Table 1

|  | Processor | Address <br> bus | Data bus | Main <br> memory | Keyboard | Visual <br> display <br> unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A | $\bigcirc$ |  | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| B | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| C | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| D | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| E | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |


| $\mathbf{0}$ | $\mathbf{2} \quad$ The internal buses in a computer use parallel communication while most peripherals |
| :--- | :--- | :--- | communicate with a computer using serial communication.


| $\mathbf{0}$ | $\mathbf{2}$ | $\mathbf{1}$ Explain the differences between the ways in which parallel and serial communication |
| :--- | :--- | :--- | :--- | is carried out.

[2 marks]
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Most peripherals, such as printers and keyboards, communicate with a computer using a serial connection.

| $\mathbf{0}$ | $\mathbf{2}$ | 2 | Apart from the widespread availability of USB (Universal Serial Bus) ports, explain |
| :--- | :--- | :--- | :--- | why peripherals usually use a serial communication method such as USB instead of parallel communication.

[1 mark]
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In communications systems, a distinction is made between the bit rate and the baud rate.

| $\mathbf{0}$ | 2 | $\mathbf{3}$ Define the term baud rate. |
| :--- | :--- | :--- |

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| $\mathbf{0}$ | $\mathbf{2} .4$ | $\mathbf{4}$ Explain how it is possible for the bit rate to be higher than the baud rate. |
| :--- | :--- | :--- |

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| 0 | 3 | A burglar alarm system is to be implemented that has the following sensors: |
| :--- | :--- | :--- |

- a door sensor D that outputs TRUE when the door is open and FALSE when the door is shut
- a pressure mat sensor $\mathbf{M}$ that outputs TRUE while a weight is detected on it and FALSE when no weight is detected on it.

The alarm also has a key $\mathbf{K}$ that turns the alarm on and off. $\mathbf{K}$ outputs a TRUE signal when the alarm is switched on and FALSE when the alarm is off.

The alarm output A sounds a bell. It should be TRUE if:

- the alarm is on AND
- either of the sensors $\mathbf{D}$ or $\mathbf{M}$ are set to the value TRUE.

| 0 | 3 | 1 | In the space below, draw a logic circuit that will behave as described above for the |
| :--- | :--- | :--- | :--- | inputs $\mathbf{D}, \mathbf{M}$ and $\mathbf{K}$ and the output $\mathbf{A}$.



| $\mathbf{0}$ | $\mathbf{3} .2$ | Write a Boolean expression to represent the logic of this alarm system. |
| :--- | :--- | :--- | :--- |


| $\mathbf{0}$ | $\mathbf{3}$ | $\mathbf{3}$ In this alarm system, the alarm bell will sound only while the door is open or a weight |
| :--- | :--- | :--- | is placed on the pressure mat. If someone who has stepped on to the mat moves off it, or an open door is closed, the alarm bell will stop ringing.

A D-type flip-flop could be incorporated into the logic circuit so that the alarm bell would continue to sound after a person closed the door or moved off the pressure mat.

Explain how this could be achieved. In your answer refer to:

- why a D-type flip-flop would be suitable for this task
- where the D-type flip-flop would need to be inserted into the circuit
- what additional input the D-type flip-flop would need.
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Turn over for the next question

| $\mathbf{0}$ | $\mathbf{4}$ The phrase "Internet of Things" is used to describe the connection of many everyday |
| :--- | :--- | devices such as home heating controls, utility meters, cars and environmental sensors to the Internet. It is believed that tens of billions of devices will be connected to the Internet of Things by the end of the decade.

One anticipated use of the Internet of Things is to monitor the food that consumers have inside their fridges. This data could be gathered automatically from consumers' devices by retailers who sell food. Retailers could use the data to analyse consumer consumption habits or automatically prepare deliveries for customers.

In the context of an Internet connected fridge, discuss the technologies that will be required to make the Internet of Things work.

You may wish to consider how the data might be captured, how networking technologies are changing to provide the necessary infrastructure, and how the data gathered by retailers could be stored and processed, from a hardware and software viewpoint.
[12 marks]
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| $\mathbf{0}$ | $\mathbf{5} \quad$ The icon in Figure $\mathbf{2}$ is represented in a computer's memory as a bitmap image. $. ~ . ~$ |
| :--- | :--- |

Figure 2


Four different colours have been used in the icon.

Row 1 of the icon is represented in the computer's memory as the bit pattern:

| 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |


| 0 | 5 | 1 | What are the bit patterns that have been used to represent a grey pixel and a white |
| :--- | :--- | :--- | :--- | pixel?

Grey pixel: $\qquad$ White pixel: $\qquad$
,
volic pict.

| $\mathbf{0}$ | $\mathbf{5} \cdot 2$ State one possible 20-bit representation for Row $\mathbf{4}$ of the icon in Figure 2. |
| :--- | :--- |




| 0 | 5 | 3 | Calculate the number of bytes required to represent all the pixel data in the icon as a |
| :--- | :--- | :--- | :--- | bitmap.

Show your working.
[2 marks]
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Answer:

| 0 | 5 | 4 |
| :--- | :--- | :--- |
| 4 |  |  | | 0 | 5 | 3 |
| :--- | :--- | :--- | :--- | State one item of metadata that would be stored in a bitmap file.

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Run-length encoding (RLE) is an example of a compression method that could be used to reduce the amount of memory required to store the icon in Figure 2.

| 0 | 5 | 5 | Describe the principle used by RLE to compress a file and explain why RLE is an |
| :--- | :--- | :--- | :--- | appropriate compression method for compressing images such as icons.

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| $\mathbf{0}$ | 6 | Figure 3 shows the structure of an example machine code instruction, taken from the |
| :--- | :--- | :--- | instruction set of a particular processor.

Figure 3

| Opcode |  |  |  |  |  | Operand(s) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Basic Machine Operation |  |  |  |  | Addressing Mode |  |  |  |  |  |  |  |  |
| 0 | 1 | 1 | 0 | $1{ }^{1} 0$ | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 1 |  |


| 0 | 6 | $\mathbf{1}$ How many different basic machine operations could be supported by the instruction |
| :--- | :--- | :--- | set of the processor used in the example in Figure 3?

Figure 4 shows an assembly language program together with the contents of a section of the main memory of the computer that the program will be executed on.

The assembly language instruction set that has been used to write the program is listed in Table 2.The lines of the assembly language program have been numbered to help you answer question parts \begin{tabular}{|l|l|l|l|l|l|}
\hline $\mathbf{0}$ \& 6 <br>
\hline

 to 

$\mathbf{0}$ \& $\mathbf{6}$ <br>
\hline
\end{tabular}

Figure 4

| Line | Command |
| :---: | :--- |
| 1 | MOV R2, \#100 |
| 2 | LDR R3, 101 |
| 3 | ADD R2, R2, R3 |
| 4 | LSL R3, R2, \#1 |
| 5 | HALT |


| Memory <br> Address <br> (in decimal) | Main Memory <br> Contents <br> (in decimal) |
| :---: | :---: |
| 100 | 23 |
| 101 | 10 |
| 102 | 62 |
| 103 | 18 |


| $\mathbf{0}$ | 6 | 2 | What value will be stored in register R2 immediately after the command in line 1 has |
| :--- | :--- | :--- | :--- | been executed?


| $\mathbf{0}$ | 6.3 | $\mathbf{3}$ What value will be stored in register R2 immediately after the program has executed |
| :--- | :--- | :--- | the commands from line 1 through to line 3 ?

[1 mark]
$\qquad$

| 0 | 6 | 4 | What value will be stored in register R3 after the complete program has finished |
| :--- | :--- | :--- | :--- | executing?

$\qquad$

Table 2

| LDR Rd, <memory ref> | Load the value stored in the memory location specified by <memory ref> into register d. |
| :---: | :---: |
| STR Rd, <memory ref> | Store the value that is in register d into the memory location specified by <memory ref>. |
| ADD Rd, Rn, <operand2> | Add the value specified in <operand2> to the value in register $n$ and store the result in register d . |
| SUB Rd, Rn, <operand2> | Subtract the value specified by <operand2> from the value in register n and store the result in register d . |
| MOV Rd, <operand2> | Copy the value specified by <operand2> into register d. |
| CMP Rn, <operand2> | Compare the value stored in register n with the value specified by <operand2>. |
| B <label> | Always branch to the instruction at position <label> in the program. |
| B<condition> <label> | Conditionally branch to the instruction at position <label> in the program if the last comparison met the criteria specified by the <condition>. Possible values for <condition> and their meaning are: <br> - EQ: Equal to. <br> - NE: Not equal to. <br> - GT: Greater than. <br> - LT: Less than. |
| AND Rd, Rn, <operand2> | Perform a bitwise logical AND operation between the value in register n and the value specified by <operand2> and store the result in register d . |
| ORR Rd, Rn, <operand2> | Perform a bitwise logical OR operation between the value in register n and the value specified by <operand2> and store the result in register d. |
| EOR Rd, Rn, <operand2> | Perform a bitwise logical exclusive or (XOR) operation between the value in register n and the value specified by <operand2> and store the result in register d . |
| MVN Rd, <operand2> | Perform a bitwise logical NOT operation on the value specified by <operand2> and store the result in register d. |
| LSL Rd, Rn, <operand2> | Logically shift left the value stored in register n by the number of bits specified by <operand2> and store the result in register d. |
| LSR Rd, Rn, <operand2> | Logically shift right the value stored in register $n$ by the number of bits specified by <operand2> and store the result in register d. |
| HALT | Stops the execution of the program. |

## Interpretation of <operand2>

<operand2> can be interpreted in two different ways, depending upon whether the first symbol is a \# or an R:

- \# - use the decimal value specified after the \#, eg \#25 means use the decimal value 25.
- Rm - use the value stored in register $m$, eg R6 means use the value stored in register 6 .

The available general purpose registers that the programmer can use are numbered 0 to 12 .

## Question 6 continues on the next page



| $\mathbf{0}$ | $\mathbf{7}$ | Figure 5 shows the physical topology of a local area network (LAN) and its connection |
| :--- | :--- | :--- | to the Internet. The LAN uses the IPv4 protocol.

Figure 5


| $\mathbf{0}$ | $\mathbf{7}$. | $\mathbf{1}$ State suitable IP addresses for: |
| :--- | :--- | :--- |

[2 marks]
The 'Router 2' port labelled $\mathbf{A}$ : $\qquad$
The computer network interface card labelled B

| 0 | 7 | 2 |
| :--- | :--- | :--- | is achieved.

[2 marks]
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$\qquad$
$\qquad$
$\qquad$
Question 7 continues on the next page
Laptop computers connect to the network using WiFi. They use carrier sense multiple access with collision avoidance (CSMA/CA) to determine when to transmit data.

| $\mathbf{0}$ | 7 | 3 |
| :--- | :--- | :--- |

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Each packet of data transmitted around the LAN includes a checksum, which is used for error detection.

| $\mathbf{0}$ | $\mathbf{7}$ | $\mathbf{4}$ |
| :--- | :--- | :--- |

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| $\mathbf{0}$ | $\mathbf{8}$ | A particular computer uses a normalised floating point representation with an 8-bit |
| :--- | :--- | :--- | mantissa and a 4-bit exponent, both stored using two's complement.

Four bit patterns that are stored in this computer's memory are listed in Figure 6 and are labelled A, B, C, D. Three of the bit patterns are valid floating point numbers and one is not.

## Figure 6



| 0 | 8 | 1 |
| :--- | :--- | :--- | A, B, C or $\mathbf{D}$ to indicate which bit pattern in Figure $\mathbf{6}$ is an example of the type of value described in the Value description column.

Do not use the same letter more than once.

## Table 3

| Value description | Correct letter (A-D) |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| A positive normalised value | A | O | B | ( |  | C | 0 |  |

Question 8 continues on the next page

| $\mathbf{0}$ | $\mathbf{8} \cdot \mathbf{2}$ The following is a floating point representation of a number: |
| :--- | :--- |



Mantissa


Exponent

Calculate the decimal equivalent of the number. Show how you have arrived at your answer.
$\qquad$
$\qquad$
Answer:

| $\mathbf{0}$ | $\mathbf{8}$ | $\mathbf{~}$ | $\mathbf{3}$ |
| :--- | :--- | :--- | :--- |
| Write the normalised floating point representation of the negative decimal value -6.75 |  |  |  | in the boxes below. Show how you have arrived at your answer.

$\qquad$
$\qquad$
$\qquad$
$\qquad$
Answer:


Exponent

An alternative two's complement format representation is proposed. In the alternative representation 6 bits will be used to store the mantissa and 6 bits will be used to store the exponent.

Existing Representation (8-bit mantissa, 4-bit exponent):


Proposed Alternative Representation (6-bit mantissa, 6-bit exponent):


| $\mathbf{0}$ | $\mathbf{8}$ | $\mathbf{4}$ | Explain the effects of using the proposed alternative representation instead of the |
| :--- | :--- | :--- | :--- | existing representation.

$\qquad$
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## Turn over for the next question

| $\mathbf{0}$ | $\mathbf{9}$ A school stores information about its sports day in a relational database. |
| :--- | :--- | :--- |

The details of the track events are stored using the three relations in Figure 7.

Figure 7
Athlete (AthleteNumber, Forename, Surname, Class, Gender, DateOfBirth)
Race (RaceNumber, Gender, Distance, Type, StartTime)
RaceEntryAndResult (RaceNumber, AthleteNumber, TimeSet)

Each athlete who takes part in a race is given a unique AthleteNumber. Athletes can run in more than one race. If they do, they keep the same AthleteNumber for the entire day.

Many races are run throughout the day. An example race would be the boys 80 m hurdles, the third race of the day, which starts at 13:30. The entry in the Race table for this race is shown in Table 4:

## Table 4

| RaceNumber | Gender | Distance | Type | StartTime |
| :---: | :---: | :---: | :---: | :---: |
| 3 | Boys | 80 | Hurdles | $13: 30$ |

When an athlete is entered into a race, a record of the entry is created in the RaceEntryAndResult table. Initially, the TimeSet is recorded as 00:00.00 (meaning 0 minutes, 0 seconds, 0 hundredths of a second) to indicate that the race has not yet been run. After the race has been run, if the athlete successfully completes it, then their TimeSet value is updated to record the time that they achieved in minutes, seconds and hundredths of a second. The TimeSet value remains at 00:00.00 for athletes who fail to complete the race.

The primary keys in the Athlete and Race relations have been identified in Figure 7 by underlining them. The correct primary key for the RaceEntryAndResult relation has not been identified.

| $\mathbf{0}$ | $\mathbf{9} \cdot 1$ In Figure 8 below, underline the appropriate attribute name(s) to identify the correct |
| :--- | :--- | :--- | primary key for this relation.

## Figure 8

RaceEntryAndResult(RaceNumber, AthleteNumber, TimeSet)

Define what it means for a database to be fully normalised.
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$\qquad$
$\qquad$

| $\mathbf{0}$ | $\mathbf{9}$. | $\mathbf{3}$ On the incomplete Entity-Relationship diagram below show the degree of the three |
| :--- | :--- | :--- | relationships that exist between the entities.



Athlete number 27 is to be entered into race number 6.

[2 marks]
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Question 9 continues on the next page

Figure 7 is repeated below.
Figure 7 (repeated)
Athlete(AthleteNumber, Forename, Surname, Class, Gender, DateOfBirth)
Race(RaceNumber, Gender, Distance, Type, StartTime)
RaceEntryAndResult(RaceNumber, AthleteNumber, TimeSet)
Athlete number 27 sets a time of 0:18.76 ( 0 minutes, 18 seconds, 76 hundredths of a second) for race number 6 .

| $\mathbf{0}$ | $\mathbf{9}$ | $\mathbf{5}$ | Write the SQL commands that are required to update the athlete's entry for this race, |
| :--- | :--- | :--- | :--- | to store this time in the TimeSet field.

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|  | The competition organisers want to produce a list of all of the athletes who took part in <br> race number 6 with the athlete who won (set the lowest time) at the top and the other <br> athletes below the winner in the order in which they finished. |
| :--- | :--- | :--- |
| Only athletes who finished the race should be included in the list. |  |
| The following information should appear for each athlete: AthleteNumber, Forename, |  |
| Surname and TimeSet. |  |
| $\mathbf{0} \mid \mathbf{9} \cdot \mathbf{6}$ Write an SQL query to produce the list. |  |

$\qquad$
$\qquad$

The database system is to be extended for use in an inter-school athletics league.
Users at any school in the county will be able to access the system to input the results of races.

It is possible that two users might try to access or update the system at the same time.

| $\mathbf{0}$ | $\mathbf{9} .7$ | $\mathbf{7}$ Explain the conditions under which simultaneous access to a database could cause a |
| :--- | :--- | :--- | :--- | problem, and how this could be dealt with.

[3 marks]
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| $\mathbf{1}$ | $\mathbf{0}$ Two computers, $\mathbf{A}$ and $\mathbf{B}$, are involved in a secure communication that uses |
| :--- | :--- | asymmetric encryption. $\mathbf{A}$ is sending a message to $\mathbf{B}$.

Each computer has a public key and a private key.

| $\mathbf{1}$ | $\mathbf{0} \cdot \mathbf{1}$ Complete the missing words in the following paragraph. |
| :--- | :--- | :--- |

A will encrypt the message using $\qquad$ key. The message will be decrypted by $\mathbf{B}$ using $\qquad$ key.

The security of the communication could be improved by the addition of a digital signature.

| 1 | 0. |
| :--- | :--- |
| 2 | State two benefits of including a digital signature. |

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| 1 | 1 | 1 |
| :--- | :--- | :--- | Table 5 lists six Boolean equations. Three of them are correct, the others are not. Shade the lozenges next to the three equations are correct.

## Table 5

| Equation | Correct? (Shade three) |
| :--- | :---: |
| $A \cdot \bar{A}=1$ | $O$ |
| $A+B=\overline{\bar{A}} \cdot \overline{\bar{B}}$ |  |
| $A+1=1$ | $O$ |
| $A \cdot(A+B)=A$ |  |
| $A+(A \cdot B)=B$ |  |
| $A \cdot 1=1$ |  |


| 1 | $\mathbf{1} \cdot \mathbf{2}$ Use Boolean algebra to simplify the following expression: |
| :--- | :--- |

$$
\overline{\overline{\mathrm{A}}+\overline{\mathrm{B}}}+\mathrm{B} \cdot \overline{\mathrm{~A}}
$$

Show your working.
$\qquad$
$\qquad$
$\qquad$
$\qquad$
$\qquad$
Answer:

## Turn over for the next question

| $\mathbf{1}$ | $\mathbf{2}$ | In a functional programming language, a recursively defined function named map and |
| :--- | :--- | :--- | a function named double are defined as follows:

```
map f [] = []
map f (x:xs) = f x : map f xs
double x = 2 * x
```

The function map has two parameters, a function $f$, and a list that is either empty (indicated as []), or non-empty, in which case it is expressed as ( $\mathrm{x}: \mathrm{xs}$ ) in which x is the head and xs is the tail, which is itself a list.

| 1 | 2 | 1 |
| :--- | :--- | :--- | [ 1, 2, 3, 4 ].

Table 6

| Head |  |
| :--- | :--- |
| Tail |  |

The result of making the function call double 3 is 6 .

| 1 | 2 | 2 |
| :--- | :--- | :--- |

## Table 7

| Function Call | Result |
| :--- | :--- |
| map double $[1,2,3,4]$ |  |

 steps that you followed.
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END OF QUESTIONS

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